METHOD FOR REDUCING DC OFFSET ERROR FOR SINGLE ENDED AND DIFFERENTIAL SIGNALS

Abstract

A feedback signal generated as a function of a peak amplitude detected in an 5 input signal provides bias to reduce DC offset. A peak in the input signal is compared with a selected signal level to align an output signal relative to a reference signal level. The output signal is generated by comparing the reference signal level and the biased input signal. Single ended signals are compared with a reference 10 level and differential input signals are compared with each other to reduce jitter.

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